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② Applicant: MITSUBISHI DENKI KABUSHIKI KAISHA
2-3, Marunouchi 2-chome
Chiyoda-ku
Tokyo 100 (JP)

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② Inventor: Hirasawa, Kazuhiro, c/o MITSUBISHI DENKI K.K.
Tsushinki Seisakusho,
1-1, Tsukaguchi-honmachi
8-chome, Amagasaki-shi, Hyogo 661 (JP)

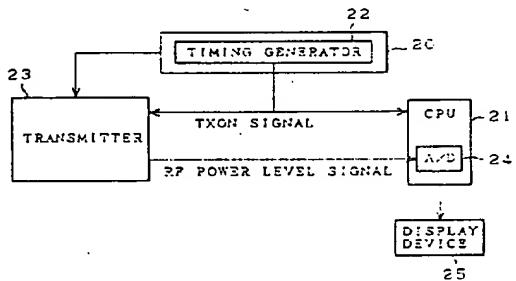
② Designated Contracting States:
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② Representative: Sajda, Wolf E., Dipl.-Phys. et al
MEISSNER, BOLTE & PARTNER
Widenmayerstrasse 48
D-80538 München (DE)

② Apparatus for monitoring the transmission power level in a TDMA communications system.

② Disclosed herein is a transmission power level monitoring apparatus suitable for use in a digital communication system using a TDMA method. The transmission power level monitoring apparatus detects start and end times of a transmission slot in each TDMA frame. An A/D converter (24) is triggered at the start and end times so as to convert transmission power into a digital signal. Then it is decided whether or not the transmission power is in an improper state during a transmission slot and a nontransmission slot in each TDMA frame. If the answer is yes, then suitable measures are taken, such as a display of a message indicative of its improper state on a display device (25), the transmission of data indicative of its improper state to another system (30) and/or turning off (26) a power source for a power amplifier of a transmitter (23).

F-I G. 1



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BACKGROUND OF THE INVENTION1. Field of the Invention:

The present invention relates to a mobile TDMA communication system for transmitting data therefrom and receiving the same therein in accordance with a TDMA method, and particularly to an RF power level monitoring apparatus suitable for use in a mobile TDMA communication system.

2. Description of the Prior Art:

FIG. 6 is a view showing the structure of an RF power level monitoring apparatus which has conventionally been employed in a mobile analog communication system. In FIG. 6, reference numeral 121 indicates a CPU of the mobile analog communication system and reference numeral 123 indicates a transmitter including a transmission and reception antenna (not shown). Further, reference numeral 124 indicates an A/D converter for converting an RF power level of a transmission signal frequency-converted into an RF band and whose power has been amplified, into a digital value.

In order to measure an analog level of a signal sent from the transmitter 123, the CPU 121 outputs a triggering signal to the A/D converter 124. The A/D converter 124 converts the analog level of the signal sent from the transmitter 123 into digital data in response to the triggering signal supplied from the CPU 121. When the conversion of the analog level into the digital data has been completed, the A/D converter 124 delivers a conversion end signal indicative of its completion to the CPU 121. When the CPU 121 recognizes the delivery of the conversion end signal from the A/D converter 124, the CPU 121 reads the digital data converted by the A/D converter 124 and makes a decision as to whether or not the analog level of the signal transmitted from the transmitter 123 is normal.

If it is decided that the analog level of the signal is in an improper state, then the CPU 121 displays the contents of its improper state on a display device of the mobile analog communication system. Subsequently the CPU 121 turns off an RF power amplifier or sends a signal indicative of its improper state to a system connected to the mobile analog communication system. When the conventional power level monitoring apparatus having the above-described construction is applied to an analog communication system whose output to be transmitted is constant, the transmission power can be monitored accurately. However, a transmission signal is burst-transmitted during a few milliseconds in a TDMA (Time Division Multiple Access) system. Therefore, the power to be transmitted cannot be measured with accuracy when the afore-

mentioned power monitoring apparatus is used.

SUMMARY OF THE INVENTION

The present invention has been made to solve the aforementioned problem. Therefore it is an object of the present invention to provide a transmission power level monitoring apparatus capable of accurately measuring power to be transmitted from a digital communication system using a TDMA system and recognizing an improper state of the power.

In order to achieve the above object, the transmission power level monitoring apparatus of the present invention detects start and end times of a transmission slot in each TDMA frame. An A/D converter is triggered at the start and end times so as to convert the respective transmission power into a digital signal. Then, it is decided whether or not the transmission power is in an improper state during the transmission slot and a nontransmission slot in each TDMA frame. If the transmission power is in an improper state, then measures are taken such as a display of a message indicative of its improper state on a display device, transmission of data indicative of its improper state to other system and/or turning off a power source for a power amplifier of a transmitter.

The above and other objects, features and advantages of the present invention will become apparent from the following description and the appended claims, taken in conjunction with the accompanying drawings in which preferred embodiments of the present invention are shown by way of illustrative examples.

BRIEF DESCRIPTION OF THE DRAWINGS

40 FIG. 1 is a view showing the structure of an RF power level monitoring apparatus according to a first embodiment of the present invention, which is employed in a mobile TDMA communication system;

45 FIG. 2 is a view for describing the timing of slots assigned to TDMA reception and transmission channels, respectively, and the timing of RXON and TXON signals, respectively;

50 FIG. 3 is a view illustrating the structure of an RF power level monitoring apparatus according to a second embodiment of the present invention, which is employed in a mobile TDMA communication system;

55 FIG. 4 is a view depicting the structure of an RF power level monitoring apparatus according to a third embodiment of

the present invention, which is employed in a mobile TDMA communication system;

FIG. 5 is a view showing the structure of an RF power level monitoring apparatus according to a fourth embodiment of the present invention, which is employed in a mobile TDMA communication system; and

FIG. 6 is a view illustrating the structure of an RF power level monitoring apparatus employed in a conventional mobile analog communication system.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

First embodiment

A first embodiment according to the present invention will be described hereinafter with reference to the accompanying drawings. FIG. 1 is a view showing the structure of an RF power level monitoring apparatus according to a first embodiment of the present invention, which is employed in a mobile TDMA communication system. Reference numeral 20 indicates a main CPU for electrically processing a transmission signal and a reception signal in accordance with a TDMA procedure. Reference numeral 21 designates a sub CPU which comprises a display controller for producing data to be displayed on a display device 25 and outputting the same therefrom, a device for inputting a detected RF power level of an RF power level signal, an A/D converter 24 for converting the RF power level of the signal into digital data, a TXON signal edge detector for detecting both the leading and trailing edges of a TXON signal shown in FIG. 2(d), the signal being indicative of the fact that data is in transmission, etc.

Reference numeral 22 indicates a timing generator for producing and outputting the TXON signal indicative of the situation that the data is in transmission. This timing generator 22 is provided within the main CPU 20. Reference numeral 23 designates a transmitter employed in the mobile TDMA communication system, which is provided with an RF power level detector for detecting the RF power level of the transmission signal. The transmitter 23 is electrically connected to a transmission and reception antenna or a transmission and reception branching filter.

FIG. 2 is a view for describing slots assigned to reception and transmission channels used under the TDMA procedure, and RXON and TXON signals.

When communication is made in the slot "0", the TXON signal shown in FIG. 2(d) is output from

the timing generator 22 of the CPU 20 upon transmission. The TXON signal is output to the transmitter 23 and the TXON signal edge detector of the sub CPU 21. The TXON signal edge detector of the sub CPU 21 detects both the leading and trailing edges of the TXON signal. The sub CPU 21 triggers the A/D converter 24 at the timing at which the TXON signal edge detector has detected the leading edge of the TXON signal in order to activate the A/D converter 24 thereby.

The A/D converter 24 converts the RF power level of the transmission signal, which has been detected by the RF power level detector of the transmitter 23 into digital data. When the conversion of the RF power level into the digital data has been completed, the A/D converter 24 informs the sub CPU 21 of its completion.

The sub CPU 21 reads the digital data converted by the A/D converter 24 and recognizes that data read by detecting the leading edge of the TXON signal is of RF power level data at the time of transmission. Then, based on the read digital data, the sub CPU 21 decides whether or not the RF power level of the transmission signal is normal.

Data indicative of the normal range of the transmission power level has been stored in the sub CPU 21 in advance. When the digital data converted by the A/D converter 24 falls within the normal range, the sub CPU 21 decides that the RF power level is normal. If the digital data falls beyond the normal range, then the sub CPU 21 decides that the RF power level is not normal.

On the other hand, the sub CPU 21 also triggers the A/D converter 24 at the timing at which the TXON signal edge detector has detected the trailing edge of the TXON signal in order to reactivate the A/D converter 24 thereby. The A/D converter 24 converts an RF power level of the transmission signal detected by the RF power level detector of the transmitter 23 into digital data. When the conversion of the RF power level into the digital data has been completed, the A/D converter 24 informs the CPU 21 of its completion. The sub CPU 21 reads the digital data converted by the A/D converter 24 and recognizes that data read by the detection of the trailing edge of the TXON signal is RF power level data at the time of nontransmission. Then, the sub CPU 21 decides based on the read digital data whether or not the RF power level of the transmission signal is normal.

That is, if the transmission power is 0 in level upon nontransmission, then the sub CPU 21 decides that the RF power level of the transmission signal is normal. If, on the other hand, the transmission power is a level different from 0, then the sub CPU 21 decides that the RF power level thereof is in an improper state.

Thus, when it is decided that the RF power levels detected upon transmission and nontransmission are in the improper state, the sub CPU 21 outputs data indicative of the contents of its improper state to the display device 25 and displays it thereon.

Incidentally, the triggering of the A/D converter 24 may not be simultaneous with the detection of the leading and trailing edges of the TXON signal. As long as its triggering is made within a time interval corresponding to one slot, the triggering of the A/D converter 24 may be effected even if a predetermined time interval has elapsed since the detection of the leading and trailing edges.

Second embodiment

A second embodiment of the present invention will be described hereinafter with reference to FIG. 3. FIG. 3 is a view showing the structure of an RF power level monitoring apparatus according to the present invention, which is employed in a mobile TDMA communication system. The same elements of structure as those in the first embodiment shown in FIG. 1 or the elements of structure similar to those in the first embodiment depicted in FIG. 1 are identified by the same reference numerals and their description will therefore be omitted. In FIG. 3, reference numeral 26 indicates a power off means for turning off an RF power amplifier. In this embodiment, the RF power amplifier 26 is turned off when a sub CPU 21 decides that an RF power level is in an improper state upon transmission or nontransmission of a transmission signal.

Third embodiment

A third embodiment of the present invention will be described hereinafter with reference to FIG. 4. FIG. 4 is a view illustrating the structure of an RF power level monitoring apparatus according to the present invention, which is employed in a mobile TDMA communication system. The same elements of a structure as those in the first embodiment shown in FIG. 1 or the elements of a structure similar to those in the first embodiment depicted in FIG. 1 are identified by the same reference numerals and their description will therefore be omitted.

In FIG. 4, reference numeral 29 indicates another system and reference numeral 30 indicates an interface circuit. In the present embodiment, when a sub CPU 21 decides that an RF power level at the time of transmission or nontransmission of a transmission signal is in an improper state, data indicative of the contents of its improper state is supplied to the other system 29 through the interface circuit 30.

This arrangement is applied to a situation where an improper state of an RF power level is transmitted to a base station as data through a telephone line, for example.

Fourth embodiment

FIG. 5 is a view showing the structure of an RF power level monitoring apparatus according to a fourth embodiment of the present invention, which is employed in a mobile TDMA communication system. In this embodiment, when a sub CPU 21 decides that an RF power level at the time of transmission or nontransmission of a transmission signal is in an improper state, either data indicative of the contents of its improper state or a signal indicative of such a state is sent to the transmitter 23. Then, the transmitter 23 transmits the data to the other system 29 to notify its improper state thereto. In this case, the other system 29 is a base station, for example. The mobile TDMA communication system notifies an improper state of transmission power to the base station via a wireless line.

Having now fully described the invention, it will be apparent to those skilled in the art that many changes and modifications can be made without departing from the spirit or scope of the invention as set forth herein.

Claims

1. A transmission power level monitoring apparatus for monitoring a level of transmission power generated in a communication system for effecting data communication in accordance with time division multiplexing access TDMA, comprising:
 - transmission start time detecting means (21) for detecting the time at which a transmission (23) of data from the communication system starts;
 - transmission power level detecting means (24) for detecting a level of transmission power during a transmission period of the communication system at a time based on the detected time; and
 - deciding means (21) for deciding whether or not the detected transmission power level is normal.
2. The apparatus according to claim 1, further comprising displaying means (25) for displaying a result of decision by the deciding means (21).
3. The apparatus according to claim 1 or 2, further comprising transmission end time de-

tecting means (21) for detecting the time at which a transmission (23) of data from the communication system ends, wherein the transmission power level detecting means (24) further detects a level of transmission power during a nontransmission period of the communication system at the time based on the detected transmission end time, and wherein the deciding means (21) makes a decision as to whether the transmission power level detected during the transmission period and the transmission power level detected during the non-transmission period are normal.

4. The apparatus according to claim 3, wherein the transmission power level detecting means (24) comprises analog transmission power level detecting means for sequentially measuring a level of transmission power generated in the communication system in an analog quantity and an analog to digital converter (24) for converting the level of transmission power detected by the analog transmission power level detecting means (24) into a digital signal at the time based on the times detected by the transmission start time detecting means (21) and the transmission end time detecting means, and wherein the deciding means (21) makes a decision based on the converted digital signal as to whether the transmission power is in an improper state.

5. The apparatus according to claim 4, wherein the analog to digital converter (24) converts the level of transmission power detected by the analog transmission power level detecting means (24) into a digital signal when a predetermined time interval has elapsed since each of the times detected by the transmission start time detecting means (21) and the transmission end time detecting means (21).

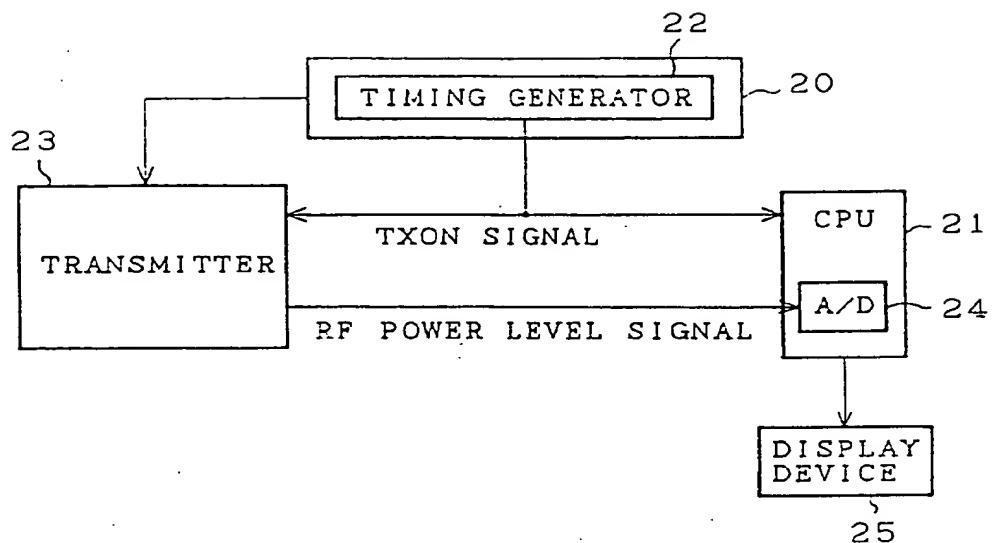
6. The apparatus according to any of claims 1 to 5, further comprising means (26) for turning off a power source for a transmitter (23) of the communication system when the deciding means (21) decides that the transmission power is in an improper state.

7. The apparatus according to any of claims 1 to 6, further comprising information transmitting means (30) for transmitting information indicative of an improper state of transmission power to another system (29) when the deciding means (21) decides that the transmission power is in an improper state.

8. The apparatus according to claim 5, wherein the information transmitting means sends the information indicative of an improper state of transmission power, which is transmitted from the transmitter (23) of the communication system as a high-frequency signal.

9. The apparatus according to claim 8, further comprising means (26) for turning off the power source for the transmitter (23) after the information has been transmitted from the transmitter as the high-frequency signal.

F I G. 1



F I G. 3

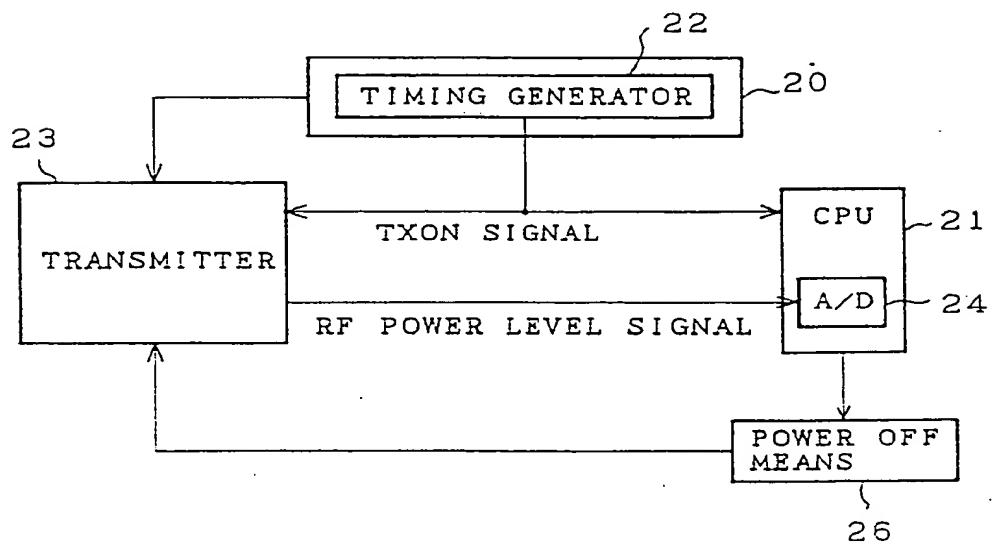


FIG. 2

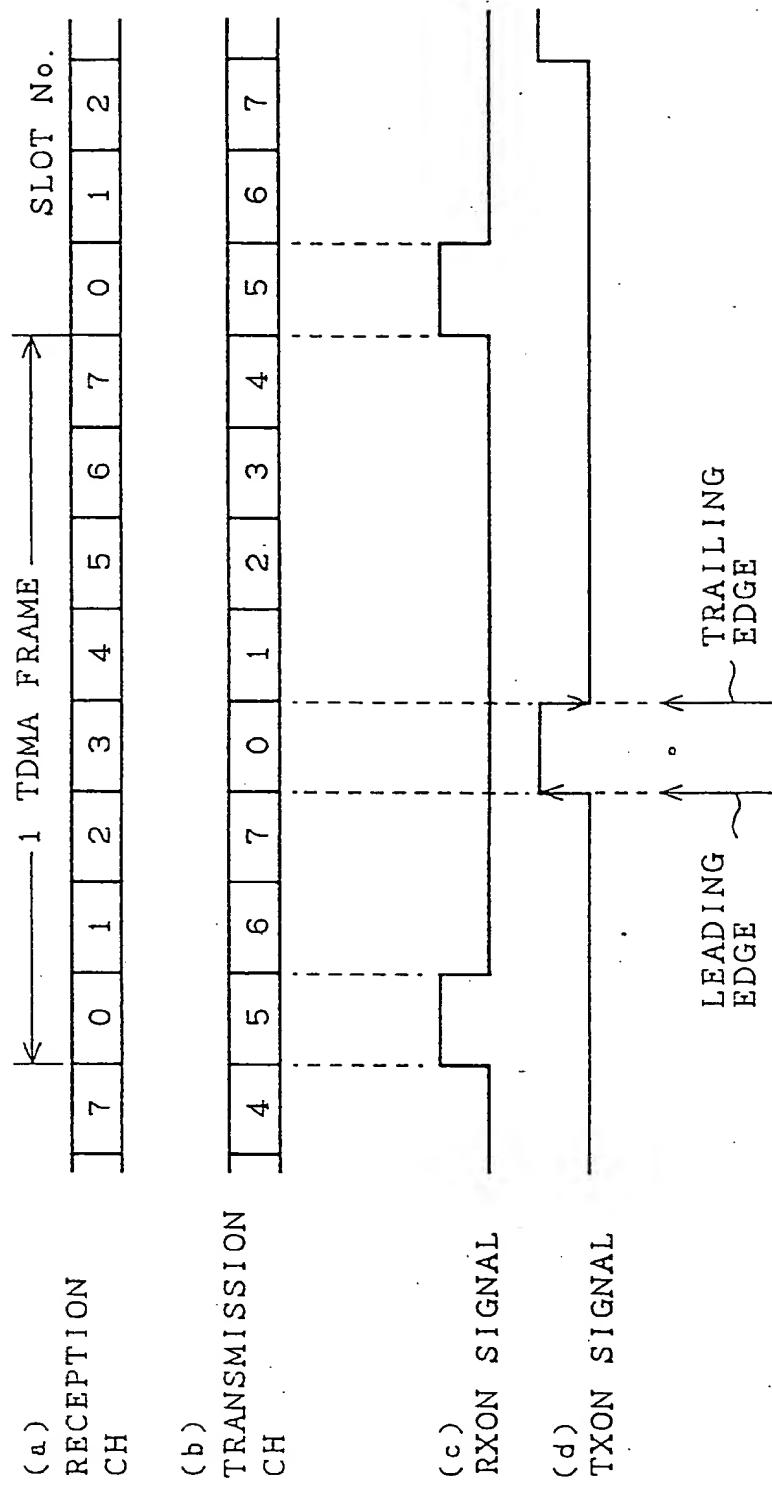


FIG. 4

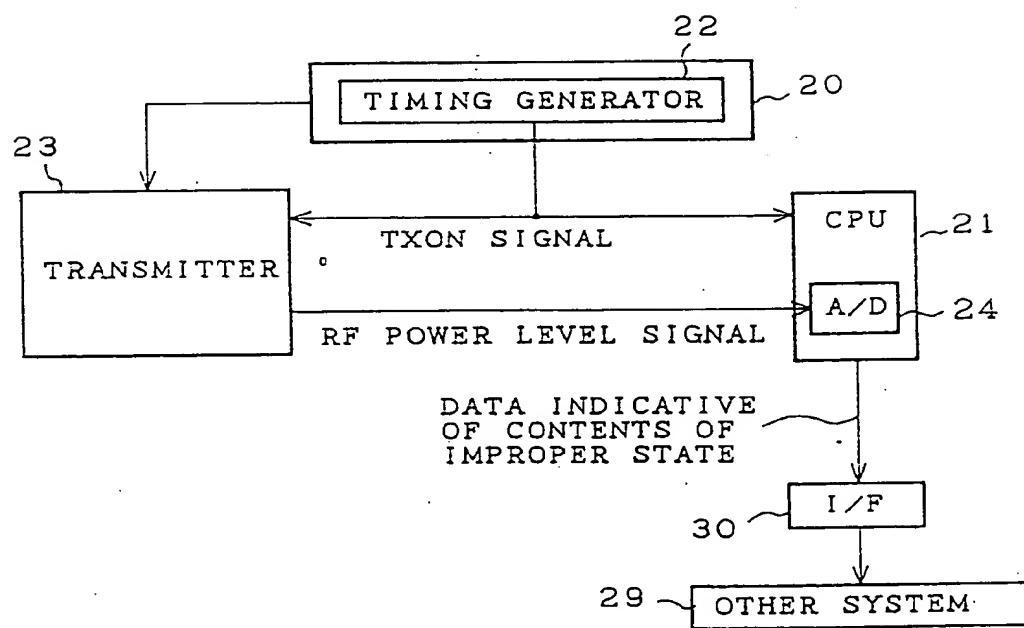


FIG. 5

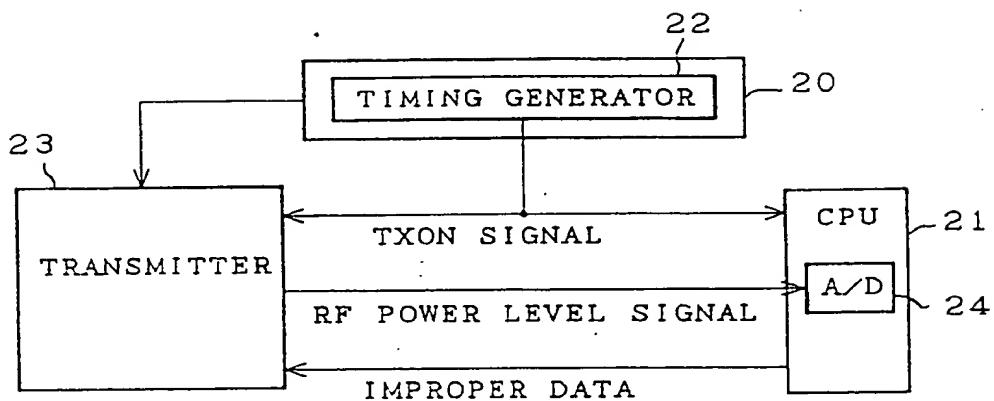


FIG. 6

